

AMENDMENTS IN THE CLAIMS:

1. (Currently Amended) An LSI, comprising:
 - a RAM for storing an intermediate code representing a command control string to be executed by a control section, and an encrypted intermediate code representing another command control string to be executed by the control section after first being decrypted;
 - a ROM for storing an interpreter execution program that is ~~capable of~~ configured to interpreting the intermediate code, and to decrypt and interpret the encrypted intermediate code; and
 - a CPU for controlling execution of the interpreter execution program,
~~wherein the RAM, the ROM, and the CPU are formed on one chip.~~
- 2-3. (Canceled)
4. (Original) An LSI according to claim 1, further comprising:
 - a recording/reproduction head for recording/reproducing information on an optical disc; and
 - an optical disc control section for controlling a motor which drives the optical disc,
 - wherein the optical disc control section is comprised within the control section, and the RAM, the ROM, the CPU and the control section are [is] formed on the one chip.
5. (Currently Amended) An optical disc apparatus, comprising:
 - an execution section for executing an interpreter execution program that is ~~capable of~~ configured to interpreting an intermediate code representing a command control string to be executed by a control section, and to decrypt and interpret an encrypted intermediate code representing another command control string to be

executed by the control section after first being decrypted, so as to generate a control command string; and

[a] the control section for controlling recording/reproduction of information on an optical disc according to the control command string.

6. (Currently Amended) An optical disc apparatus according to claim 5, wherein the execution section includes:

- a RAM for storing ~~[an]~~ the encrypted intermediate code;
- a ROM for storing the interpreter execution program; and
- a CPU for controlling execution of the interpreter execution program.

7. (Original) An optical disc apparatus according to claim 6, wherein the RAM, the ROM, and the CPU are formed on one chip.

8. (Original) An optical disc apparatus according to claim 7, wherein the control section includes:

- a recording/reproduction head for recording/reproducing information on the optical disc;
- a motor for driving the optical disc; and
- an optical disc control section for controlling the recording/reproduction head and the motor.

9. (Original) An optical disc apparatus according to claim 8, wherein the optical disc control section is formed on the one chip.

10. (Original) An optical disc apparatus according to claim 5, wherein the intermediate code is encrypted.

11. (Currently Amended) An optical disc apparatus according to claim 6, wherein:

~~the RAM can store an~~ stores the encrypted intermediate code and ~~[an] the~~
unencrypted intermediate code; and

~~the interpreter execution program can interpret both the encrypted~~
~~intermediate code and the unencrypted intermediate code.~~

12. (New) An LSI according to claim 1, wherein the RAM, the ROM, and the CPU are formed on one chip.

13. (New) An LSI according to claim 1, wherein the intermediate code represents user customized command control strings, and the encrypted intermediate code represents vendor proprietary command control strings.

14. (New) An optical disk apparatus according to claim 5, wherein the intermediate code represents user customized command control strings, and the encrypted intermediate code represents vendor proprietary command control strings.